

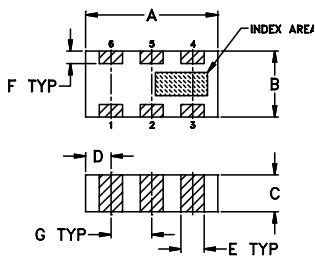
2 Way-0° 50Ω □□□ 1425 to 1900 MHz

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0)*&10000200&0)3&000	000400)*000040
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*derate linearly to 6W at 100°C ambient.

00000 000000000000	
000000 0000	
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!0 0"##00	0\$%\$0
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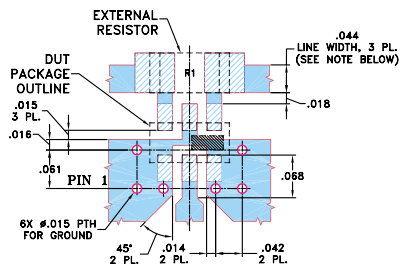
9 *****



SUGGESTED LAYOUT FOR PCB PATTERN
PATTERN TO BE WITHIN $\pm .002$

9mm 0000000000 000 0000000000 (mm)					
0	=	0	#	0	0
00 0	000%	00%9	00 0	00	000
3.20	1.60	0.94	0.61	0.56	0.30
5)					
! 00%0	.	F 00 0	G 00 %	18:02'	00 0
0.99	1.07	0.61	3.12		

Demo Board MCL P/N: TB-252
Suggested PCB Layout (PL-129)



NOTE: RESISTOR R1: 100 OHM, 1206 SIZE

1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350 WITH DIELECTRIC THICKNESS .020" $\pm$.0015". COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

 DENOTES PCB COPPER LAYOUT

 DENOTES COPPER LAM AND PATTERN FREE OF SOLDERMASK

```

00000000
0 (":*-) (&,*&")(*&$+0+)0&,0-000000*';2'
0 -5(0,0&)*,0-5**$00000(==0)>0
0 0+?0-0,0020-0)3<0003,0@0-0,?0$000 (==0)>00
0 1**<0;0003,0@0-0,?0$000<01000)>00
0 ;(1,0)(":*-) (&,*&")(*&$+0+)0&,0-00000*';2'
0 0+?0-0,0020-0)3<0003,0@0-0,?0$000(==0)>00
0 '20-0'(A0$0000 B00000B00000%0B
0 00#0,*,00,0(D0
0 )020000)3&00000(0-000000)?:*,**1>
0 5&0000003,<0)0&2(000)0(*)'
0 E*0&0+?0-0,0)0*-<0&0(-0-)
0 -5(0?*)
0 00)0,0000,<0,1

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& □□□□□□
/□!□□
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00000000 0000 0000
00000 000 00000000000000000000
0 0000000000000000000000000000

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

See our web site for RoHS Compliance methodologies and qualifications.

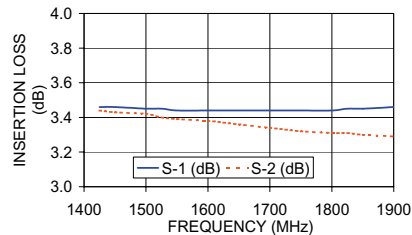
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00"00 000 0	0.00 %1(2\$%344 0000008000	4+20 4+30 0000 0000	(5) %6 00 000	(+2) 1+4 00 0000 0000 0000	4+(2) 4+10 00 0000 0000 08	%20 08

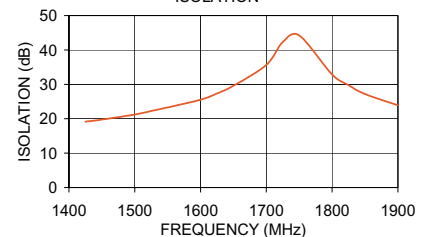
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[illegible]

SCN-2-19
INSERTION LOSS



SCN-2-19
ISOLATION



SCN-2-19
RETURN LOSS

